UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

Page 1 of 2

PATENT NO. : 6,985,823 B2 APPLICATION NO. : 10/696253

DATED : January 10, 2006 INVENTOR(S) : Fishman et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Drawings

Sheet 1, Figure 1, change the label "D_{in} 196" to --I/O 196--

Column 3

Line 17, change "that is more" to --that are more--

Column 4

Line 8, change "embodiments" to --embodiment--

Column 6

Line 47, change "is described" to -- are described--

Column 7

Lines 41 and 42, change "D_{in} ports 192, 196" to --a D_{in} port 192--

Column 8

Line 40, change "then" to --than--

Column 9

Line 7, change "138" to --128--

Line 16, change "304" to --block 304--

Column 10

Line 21, change "106" to --104--

Line 21, change "135" to --125--

Column 12

Line 21, change "ports" to --port--

Line 33, change "error" to --errors--

Line 63, change "bits groups" to --bit groups--

Column 13

Line 54, change "is gathered" to --are gathered--

Line 64, change "DUT 160" to --DUT 170--

Column 15

Line 16, change "bits" to --bit--

Column 16

Line 30, change "bits groups" to --bit groups--

Line 66, change "newly level" to --newly set level--

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Column 17

Line 14, change "456 and 458" to --460 and 462--

Signed and Sealed this

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Eighteenth Day of March, 2008

JON W. DUDAS
Director of the United States Patent and Trademark Office